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P/126-224

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Yutaka ITO et al.	Date	: July 15, 2004
Serial No.	: 10/699,223	Group Art Unit : ---
Filed	: October 30, 2003	Examiner : ---
For	: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE	

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUBMISSION OF PRIORITY DOCUMENTS

Sir:

In accordance with 35 U.S.C. §119, Applicant confirm the prior request for priority under the International Convention and submits herewith the following documents in support of the claim:

Certified Japanese Application No.:

Japanese Patent Application No. 2002-315319 filed October 30, 2002

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on July 15, 2004

Max Moskowitz

Name of applicant, assignee or
Registered Representative

Signature

July 15, 2004

Date of Signature

Respectfully submitted,

Max Moskowitz

Registration No.: 30,576

OSTROLENK, FABER, GERB & SOFFEN, LLP

1180 Avenue of the Americas

New York, New York 10036-8403

Telephone: (212) 382-0700

MM:msd